


Substitute for form 1449A/PTO		Complete If Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Application Number	10/090,944
		Filing Date	3.4.02
		First Named Inventor	Paul Luse
		Art Unit	2186
		Examiner Name	PEIKARI
		Attorney Docket Number	42P18502
Sheet	1	of	2

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.*	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
BJP ✓	1	Adaptec - Adaptec SCSI RAID 2000S Support	
BJP ✓	2	Adaptec - Adaptec SCSI RAID 2005S Support	
BJP ✓	3	Adaptec - Motherboard Partners and Compatibility	
BJP ✓	4	Adaptec - Adaptec SCSI RAID 2005S Technical Specs	
BJP ✓	5	Adpatec - Adaptec Article: Zero Channel RAID Frequently Asked Questions	
BJP ✓	6	Intel Corp., Product Brief, Intel Server Board SCB2	
BJP ✓	7	Serial ATA: High Speed Serialized AT Attachment Revision 1.0, 29 August 2001	
BJP ✓	8	Super0, Super P3TDDR, User's Manual, Revision 1.0a	
BJP ✓	9	PCI Local Bus, PCI BIOS Specification, Revision 2.1 August 26, 1994	
BJP ✓	10	PCI Local Bus, PCT-to-PCI Bridge Architecture Specification, Revision 1.1, December 16, 1998	
BJP ✓	11	PCI Local Bus, PCI Local Bus Specification, Revision 2.2, December 18, 1998	
BJP ✓	12	PCI Local Bus, Mini PCI Specification, Revision 1.0, October 25, 1999	
BJP	13	PCI Local Bus, PCI-X Addendum to the PCI Local Bus Specification, Revision 1.0a, July 24, 2000	
BJP	14	PCI Local Bus, PCI Local Bus Specification, Revision 2.3, March 29, 2002	

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Examiner Signature		Date Considered	10/29/04
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*Applicant's unique citation designation number. *Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08B (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wlr) 08/11/2003.
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